@PFDesktop\::ODMA/MHODMA/HBSR05;iManage;491903;1 NSP/KTS/BSL/jam

August 11, 2004



PATENT APPLICATION DOCKET NO.: 0717.2013-013

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

	•	• •	
		100	
м	1111	lica	,,,

Roger E. Welser, Paul M. Deluca and Noren Pan

Application No.:

10/824,697

Group:

Filed:

April 14, 2004

Examiner: Not Assigned

2813

Confirmation No.:

6799

For:

BIPOLAR TRANSISTOR WITH LATTICE MATCHED BASE LAYER

I hereby certify that this corn Postal Service with sufficient to Commissioner for Patents	OF MAILING OR TRANSMISSION rrespondence is being deposited with the United States at postage as First Class Mail in an envelope addressed s, P.O. Box 1450, Alexandria, VA 22313-1450, or is to the United States Patent and Trademark Office on:
Date	Signature Signature
j	Jane Morgan
Typed or prin	nted name of person signing certificate

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

rionar	MIN., VI. 22515 1150
Sir:	
This I	nformation Disclosure Statement is submitted: under 37 CFR 1.129(a), or (First/Second submission after Final Rejection)
[X]	under 37 CFR 1.97(b), or (Within any one of the following time periods: three months of filing national application (other than a CPA) or date of entry of the national stage in an international application; or before the mailing date of a first office action on the merits in a non-provisional application, including CPA, or a Request for Continued Examination).
[]	under 37 CFR 1.97(c) together with either: [] a Statement under 37 CFR 1.97(e), as checked below, or [] a \$180.00 fee under 37 CFR 1.17(p), or (After the 37 CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first)
[]	under 37 CFR 1.97(d) together with: [] a Statement under 37 CFR 1.97(e), as checked below, and [] a \$180.00 fee under 37 CFR 1.17(p), or (Filed after final action or notice of allowance, whichever occurs first, but on or before payment of the issue fee)
[]	under 37 CFR 1.97(i): Applicant requests that the IDS and cited reference(s) be placed in the application filewrapper. (Filed after payment of issue fee)

[X]

Statem	nent Und	der 37 (CFR 1.97(e)			
[]	Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement; or					
[]	No item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.					
Staten	nent Un	der 37 ((Patent Term Adjustment) Applies to original applications (other than design) filed on or after May 29, 2000			
[]	commi	unication treceive	information contained in the Information Disclosure Statement was cited in a form a foreign patent office in a counterpart application and this communication yed by any individual designated in § 1.56(c) more than thirty days prior to the aformation Disclosure Statement.			
[X]	Enclos	sed here	with is form PTO-1449:			
	[]	Copies	s of the cited references are enclosed.			
		[]	Since this application was filed after June 30, 2003, copies of issued U.S. patents and published U.S. applications are not required and are not being provided.			
	[X]	Applic	s of the cited references are enclosed except those entered in prior application, U.S. cation No. 09/995,079, to which priority under 35 U.S.C. 120 is claimed. The application contains copies of the cited references.			
	[]	The list	sted references were cited in the enclosed International Search Report in a erpart foreign application.			
	[X]	The "c	concise explanation" requirement (non-English references) for references AN and older 37 CFR 1.98(a)(3) is satisfied by:			
		[]	the explanation provided on the attached sheet.			
		[]	the explanation provided in the Specification.			
		[]	submission of the enclosed International Search Report.			
		[]	submission of the enclosed English-language version of a foreign Search Report and/or foreign Office Action.			
		[X]	the enclosed English language abstract of reference AN.			

the previously entered English language abstract of reference AO in the earlier application.

[]	A	pplic	ant requests that the following n	on-published pending applic	eations be considered:
Examiner's	5				
	•		U.S. Patent Application No. [], by [inventor(s)], filed [], Docket No.: []
	_		U.S. Patent Application No. [], by [inventor(s)], filed [], Docket No.: []
	-		U.S. Patent Application No. [], by [inventor(s)], filed [], Docket No.: []
			Examiner	Date	_
	[]	A copy of each above-cited app	olication, including the curre	nt claims, is enclosed.
	[]	A copy of each above-cited app those entered in prior application 35 U.S.C. 120 is claimed.	olication, including the curre on, U.S. Application No. [nt claims, is enclosed, except], to which priority under
The E	xaı nce	miner es we	is requested to return a copy of re considered with the next office	the above list of pending appear communication.	plications indicating which
It is re	equ	ested	that the information disclosed h	nerein be made of record in the	nis application.
Metho	od (of pay	yment:		
[]	A	ccom	ck for the fee noted above is enc panying Reply. A copy of this S	losed, or the fee has been inc Statement is enclosed.	cluded in the check with the
[]		lease nclos	charge Deposit Account 08-038 ed.	30 in the amount of \$[]	. A copy of this Statement is
[X]	P	lease	charge any deficiency in fees ar	nd credit any overpayment to	Deposit Account 08-0380.
			I	Respectfully submitted,	
			I	HAMILTON, BROOK, SMI	TH & REYNOLDS, P.C.
			I	Kevin T. Shaughnessy Registration No.: 51,014 Telephone: (978) 341-0036 Facsimile: (978) 341-0136	

Concord, MA 01742-9133 Dated:

Sheet 1 of 5

PTO-1449 REPRODUCED	ATTORNEY DOCKET NO. 0717.2013-013		lication no. 8 24, 697	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION	FIRST NAMED INVENTOR Roger E. Welser		FILING DATE April 14, 20	004
August 9, 2004 AU6 1 3 2004	ZEXAMINER Not Assigned	CONFI 6799	RMATION NO.	GROUP 2813

		TA TRADEME		· ·
	r"	U.S.	PATENT DOCUMENTS	
EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER Number-Kind Code (if known)	ISSUE DATE / PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE OR APPLICANT OF CITED DOCUMENT
	AA	US 2001/0040244 A1	11-15-2001	Fitzgerald, et al.
	AB	6,031,256	02-29-2000	Liu, et al.
	AC	5,606,185	02-25-1997	Nguyen, et al.
	AD	US 6,285,044 B1	09-04-2001	Bhat
	AE	US 6,150,677	11-21-2000	Tanaka et al.
	AF	US2002/0102847 A1	08-01-2002	Sharps et al.
	AG	US 6,150,667	11-21-2000	Ishizaka et al.
	АН	US 2002/0027232 A1	03-07-2002	Shigematsu et al.
	AI	4,518,979	05-21-1985	Dumke et al.
	AJ	5,371,389	12-06-1994	Matsuno et al.
	AK	5,429,957	07-04-1995	Matsuno et al.
	AA2	5,571,732	11-05-1996	Liu
	AB2	5,814,843	09-29-1998	Ohkubo
	AC2	5,858,818	01-12-1999	Ro et al.
	AD2	5,903,018	05-11-1999	Shimawaki
	AE2			
	AF2		·	
	AG2			
	AH2			
	AI2			
	AJ2			
	AK2			
	AA3			
	AB3			
	AC3			

EXAMINER	DATE CONSIDERED

2813

PTO-1449 REPRODUCED

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

August 9, 2004

(Use several sheets if necessary)

		Silect 2 Of .
ATTORNEY DOCKET NO. 0717.2013-013	APPLICATION NO. 10/824,697	
FIRST NAMED INVENTOR Roger E. Welser	FILING DATE April 14, 2	2004
EYAMDIED	CONFIRMATION NO	CROUP

6799

FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER Country Code-Number-Kind Code (if known)	DATE MM-DD-YYYY	NAME OF PATENTEE OR APPLICANT OF CITED DOCUMENT	TRANS YES_	TRANSLATION YES NO		
AL	WO 01/03194 A1	01-11-2001	Picogiga	X			
AM	WO 02/43155 A2	05-30-2002	Kopin Corporation				
AN	FR 2 795 871 A1	01-05-2001	Picogiga		X		
AO	JP 11312685	11-09-1999	Fujitsu Ltd.		Х		
AP				-			
AQ							
AL2							
AM2							
AN2							
AO2				-			
AP2							
AQ2		•					
AL3							
AM3							
AN3							
AO3							
AP3							
AQ3							
AL4							
AM4							
AN4					J		
AO4		1					
AP4							
AQ4							

Not Assigned

EXAMINER	DATE CONSIDERED

				Sheet 3 Of	
PTO-1449 REPRODUCED			PPLICATION NO. 0/824,697		
INFORMATION DISCLOSURE CITATION IN AN APPLICATION			FILING DATE April 14, 20	FILING DATE April 14, 2004	
August 9, 2004 (Use several sheets if necessary)	EXAMINER Not Assigned	CONF	CONFIRMATION NO. GROUP 2813		

	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
AR	Chang, et al., "InGaAsN/AlGaAs P-n-p heterojunction bipolar transistor," Applied Physics Letters, 79(19):2788-2790 (2000).
AS	Welser, et al., "Low V _{be} GaInAsN Base Heterojunction Bipolar Transistors," <i>IEICE Trans. Electron.</i> , E84-C(10): 1389-1393 (2001).
AT	Li, et al., "DC characteristics of MOVPE-grown Npn InGaP/InGaAsN DHBTs," Electronics Letters, 36(1): 81-83 (2000).
AU	Kohama, et al., "Using Carbon Tetrachloride for Carbon Doping Al _x Ga _{1-x} As Grown by Metalorganic Chemical Vapor Deposition," <i>Jpn. J. Appl. Phys.</i> , 34(7A): 3504-3505 (1995).
AV	Sugiura, et al., "Characterization of heavily carbon-doped InGaAsP layers grown by chemical beam epitaxy using tetrabromide," Applied Physics Letters, 73(17):2482-2484 (1998).
AW	Bhat, et al., "Growth of GaAsN/GaAs, GaInAsN/GaAs and GaInAsN/GaAs quantum wells by low-pressure organometallic chemical vapor deposition," Journal of Crystal Growth, 195: 427-437 (1998).
AX	Chang, et al., "InGaP/InGaAsN/GaAs NpN double-heterojunction bipolar transistor," Applied Physics Letters, 76(16):2262-2264 (2000).
AY	Welser, R.E., et al., "Role of Neutral Base Recombination in High Gain AlGaAs/GaAs HBT's," IEEE Transactions on Electron Devices, 46(8):1599-1607(1999).
AZ	Ahmari, D.A., et al., "High-speed InGaP/GaAs HBT's with a Strained In _χ Ga _{1-χ} As Base," <i>IEEE Electron Device Letters</i> , 17(5):226-228(1996).
AR2	Welser, R.E., et al., "Turn-on Voltage Investigation of GaAs-Based Bipolar Transistors with Ga _{1-x} In _x As ₁ , yN _y Base Layers," <i>IEEE Electron Device Letters</i> , 21(12):1-4(2000).
AS2	Low, T., et al., "InGaP HBT technology for RF and microwave instrumentation," Solid-State Electronics, 43:1437-1444(1999).
AT2	Liu, W., et al., "Current Transport Mechanism in GaInP/GaAs Heterojunction Bipolar Transistors," <i>IEEE Transactions on Electron Devices</i> , 40(8):1378-1383(1993).
AU2	Lu, Z.H., et al., "Determination of band gap narrowing and hole density for heavily C-doped GaAs by photoluminescence," Appl. Phys. Lett., 64(1): 88-90(1994).

EXAMINER	DATE CONSIDERED

PTO-1449 REPRODUCED	ATTORNEY DOCKET NO. 0717.2013-013		LICATION NO. 824,697	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION	FIRST NAMED INVENTOR Roger E. Welser		FILING DATE April 14, 2004	
August 9, 2004 (Use several sheets if necessary)	EXAMINER Not Assigned			GROUP 2813

		OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
А	AV2	Welser, R.E., et al., "High Performance Al _{0.35} Ga _{0.65} As/GaAs HBT's," IEEE Electron Device Letters, 21(5):196-199(2000).
А	AW2	Welser, R.E., et al., "Base Current Investigation of the Long-Term Reliability of GaAs-Based HBTs," GaAs Mantech, (2000).
A	AX2	Patton, G.L., et al. "Graded-SiGe-Base, Poly-Emitter Heterojunction Bipolar Transistors," <i>IEEE Electron Device Letters</i> , 10(12):534-536(1989).
A	AY2	Ida, M., et al., "InP/InGaAs DHBTs with 341-Ghz f _T at high current density of over 800 kA/cm ² ," IEEE, (2001).
А	AZ2	Kroemer, H., "Heterostructure bipolar transistors: What should we build?" J. Vac. Sci. Technol., B1(2):126-130(1983).
A	AR3	Fujihara, A., et al., "High-speed InP/InGaAs DHBTs with Ballistic Collector Launcher Structure," IEEE, (2001).
A	AS3	Nakahara, K., et al., "Continuous-wave operation of long-wavelength GaInNAs/GaAs quantum well laser," <i>Electronic Letters</i> , 32(17): 1585-1586(1996).
А	AT3	Mochizuki, K., et al., "GaInP/GaAs Collector-Up Tunneling-Collector Heterojunction Bipolar Transistors (C-Up TC-HBTs): Optimization of Fabrication Process and Epitaxial Layer Structure for High-Efficiency High-Power Amplifiers," <i>Transactions on Electron Devices</i> , 47(12):2277-2283(2000).
Α	AU3	Pan, N., et al., "Pseudomorphic In-Graded Carbon Doped GaAs Base Heterojunction Bipolar Transistors by Metal Organic Chemical Vapor Deposition," Journal of Electronic Materials, 25(7):13 (1996).
A	AV3	Ohkubo, M., et al., "Compositionally Graded C-doped In _{1-X} Ga _X As Base in InP/InGaAs D-HBTs Grown by MOCVD with Low Base Sheet Resistance and High Current Gain", <i>IEEE</i> , pp. 641-644, 1997.
A	AW3	Stockman, S. A., et al., "Carbon Doping of In _x Ga _{1-x} As By MOCVD Using CCI ₄ ", pp. 40-43, no date given.
A	AX3	Keiper, D., et al., "Metalorganic Vapour Phase Epitaxy Growth of InP-based Heterojunction Bipolar Transistors with Carbon Doped InGaAs Base Using Tertiarybutylarsine and Tertiarybutylphosphine in N ₂ Ambient", XP-001030248, <i>Jpn. J. Appl. Phys.</i> , Vol. 39:6162-6165 (2000).

EXAMINER	DATE CONSIDERED	
		1

Sheet 5 of 5

				Silect 3 Of	
PTQ-1449 REPRODUCED	ATTORNEY DOCKET NO. 0717.2013-013		LICATION NO. /824,697		
INFORMATION DISCLOSURE CITATION IN AN APPLICATION			FILING DATE April 14, 20		
August 9, 2004 (Use several sheets if necessary)	EXAMINER Not Assigned	CONF. 6799	IRMATION NO.	GROUP 2813	

AY3 Stillman, G. E., et al., "Carbon-doped InGaAs grown by MOCVD for InP/InGaAs heterojunction bipola transistors", Inst. Phys. Conf. Ser. No. 129:687-692 (1992).		OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
	AY3	Stillman, G. E., et al., "Carbon-doped InGaAs grown by MOCVD for InP/InGaAs heterojunction bipolar transistors", Inst. Phys. Conf. Ser. No. 129:687-692 (1992).

EXAMINER	DATE CONSIDERED
, , , , , , , , , , , , , , , , , , ,	